

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. **(currently amended)** A thin film transistor panel, comprising:  
a gate line with a gate electrode on a substrate;  
a gate insulating layer on the gate line;  
a semiconductor layer on the gate insulating layer;  
a conductive pattern layer with source and drain electrodes spaced apart on the semiconductor layer, the conductive pattern layer comprising a first molybdenum layer, an Ag-Al alloy layer which contains about 5 to about 10 at% of silver on the first molybdenum layer and a second molybdenum layer on the Ag-Al alloy layer;  
a passivation layer on the semiconductor layer and the conductive pattern layer, the passivation layer having a plurality of contact holes; and  
a plurality of pixel electrodes on the passivation layer, each of the pixel electrodes extending into one of the contact holes to contact the second molybdenum layer.

2. (original) The thin film transistor panel as claimed in claim 1, wherein the gate line comprises an Ag-Al alloy layer on the substrate and a molybdenum layer on the Ag-Al alloy layer.

3-4. **(canceled)**

5. (original) The thin film transistor panel as claimed in claim 1, wherein the Ag-Al alloy layer contains about 10 at% of silver.

6. **(currently amended)** A liquid crystal display, comprising:  
a top plate comprising a transparent electrode;  
a bottom plate comprising reflective electrodes of an annealed Ag-Al alloy which has been annealed at temperatures from about 200 °C to about 250 °C; and  
a liquid crystal layer sandwiched between the top plate and the bottom plate,  
wherein an image is generated by the liquid crystal display when ambient light is incident to the surface of the top plate.

7. **(currently amended)** The liquid crystal display as claimed in claim 6, further comprising a light source behind below the bottom plate wherein each of the reflective electrodes has at least one aperture defined therein such that an image is generated by the liquid crystal display when light from the light source passes through the apertures of the reflective electrodes.

8. **(currently amended)** The liquid crystal display as claimed in claim 7, wherein the bottom plate further comprises:

a plurality of parallel gate lines;  
a plurality of parallel data lines formed perpendicular to the gate lines, the gate lines and the data lines being arranged to form a matrix of pixel regions with each of the pixel regions bounded by two adjacent said gate lines and two adjacent said data lines; and  
a plurality of thin film transistors formed at intersections between the of said gate lines and data lines,  
wherein each of the reflective electrodes is respectively disposed in one of the pixel regions and functions as a pixel electrode.

9. *(canceled)*

AB  
10. **(currently amended)** The liquid crystal display as claimed in claim 6, wherein the annealed Ag-Al alloy contains about 5 to about 10 at% of silver.

11. *(canceled)*

AN  
12. **(currently amended)** The liquid crystal display as claimed in claim [[11]] 6, wherein the annealed Ag-Al alloy contains about 10 at% of silver and the reflective electrodes have a visible light reflectance greater than [[95 %]] 97%.

13-16. *(canceled)*

17. **(currently amended)** A thin film transistor panel, comprising:  
a gate line with a gate electrode on a substrate, the gate line comprising an Ag-Al alloy layer which contains about 5 to about 10 at% of silver on the substrate and a molybdenum layer on the Ag-Al alloy layer;  
a gate insulating layer on the gate line;  
a semiconductor layer on the gate insulating layer;  
a conductive pattern layer with spaced apart source and drain electrodes on the semiconductor layer;  
a passivation layer on the semiconductor layer and the conductive pattern layer, the passivation layer having a plurality of contact holes; and  
a plurality of pixel electrodes on the passivation layer, each of the pixel electrodes extending into one of the contact holes and directly contacting the molybdenum layer.

18. **(currently amended)** The thin film transistor panel as claimed in claim 17, wherein

the conductive pattern layer comprises comprising a first molybdenum layer, an Ag-Al alloy layer on the first molybdenum layer and a second molybdenum layer on the Ag-Al alloy layer.

**19-20. (canceled)**

21. (new) The liquid crystal display as claimed in claim 6, wherein the annealed Ag-Al alloy contains about 10 at% of silver and the reflective electrodes have a visible light reflectance greater than 95%.